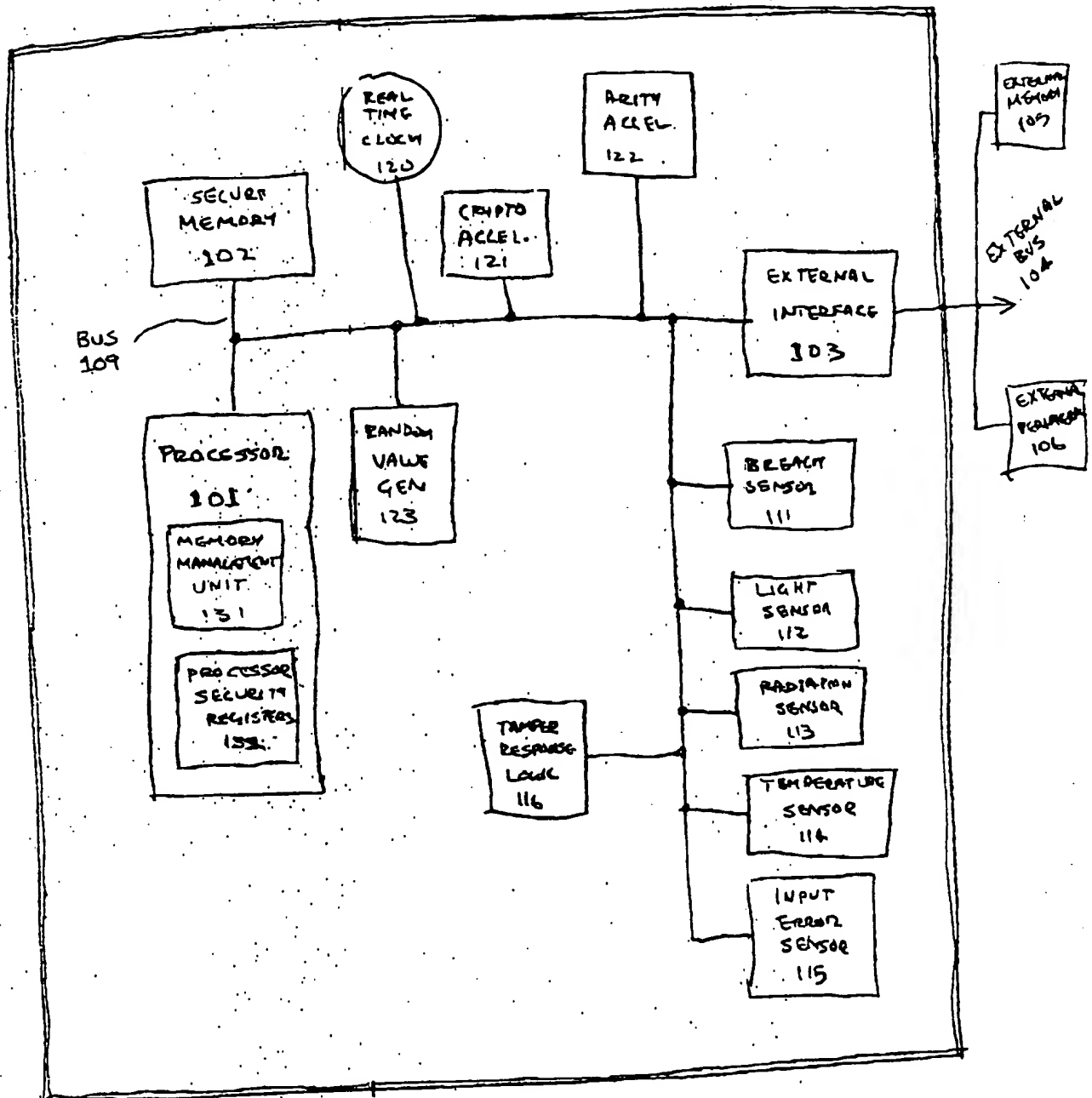


FIGURE 1A

SECURE PROCESSING UNIT 100

TAMPER-RESISTANT PACKAGE 110



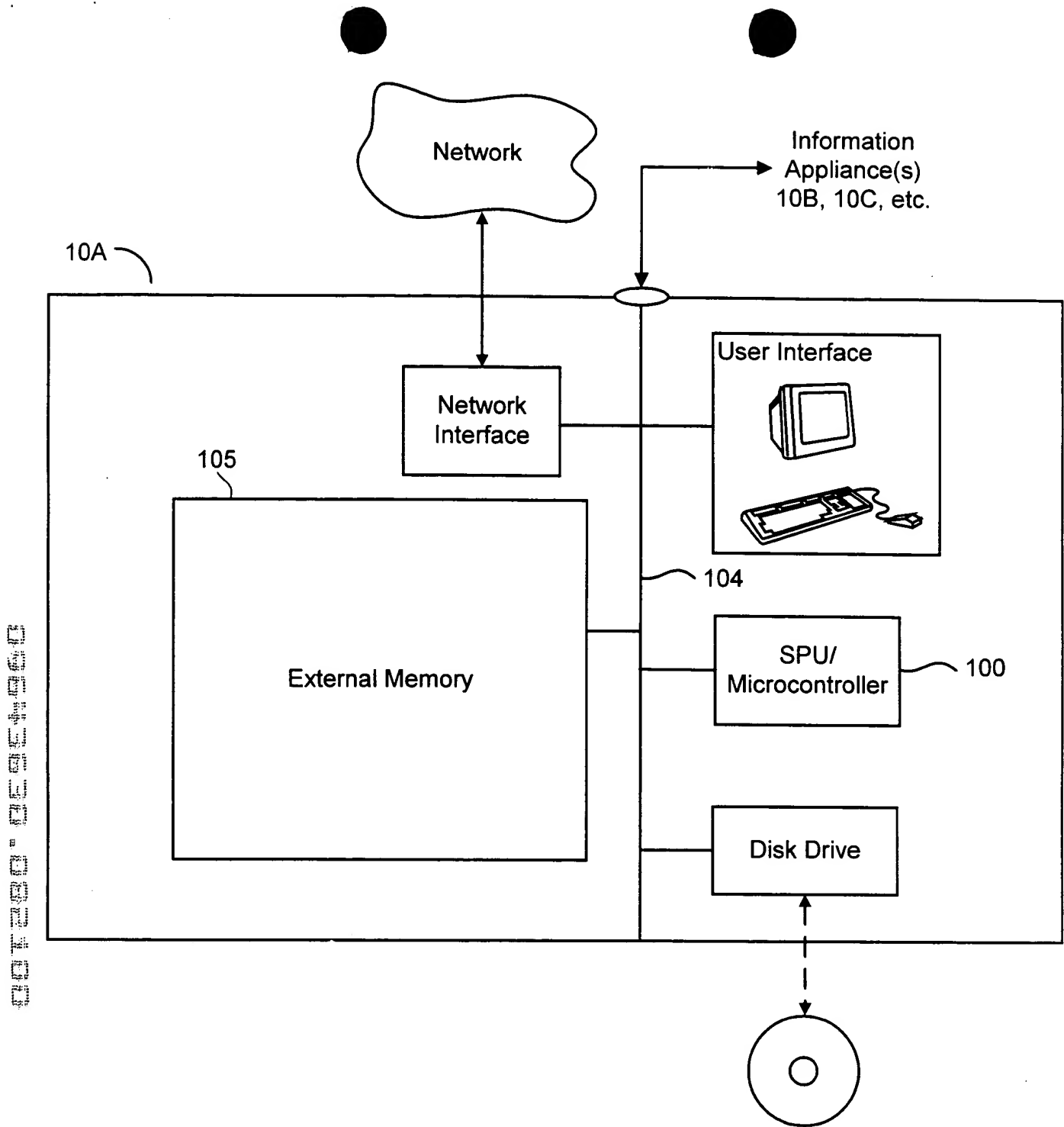


Fig. 1B

FIGURE 2

TAMPER-RESISTANT PACKAGE 110

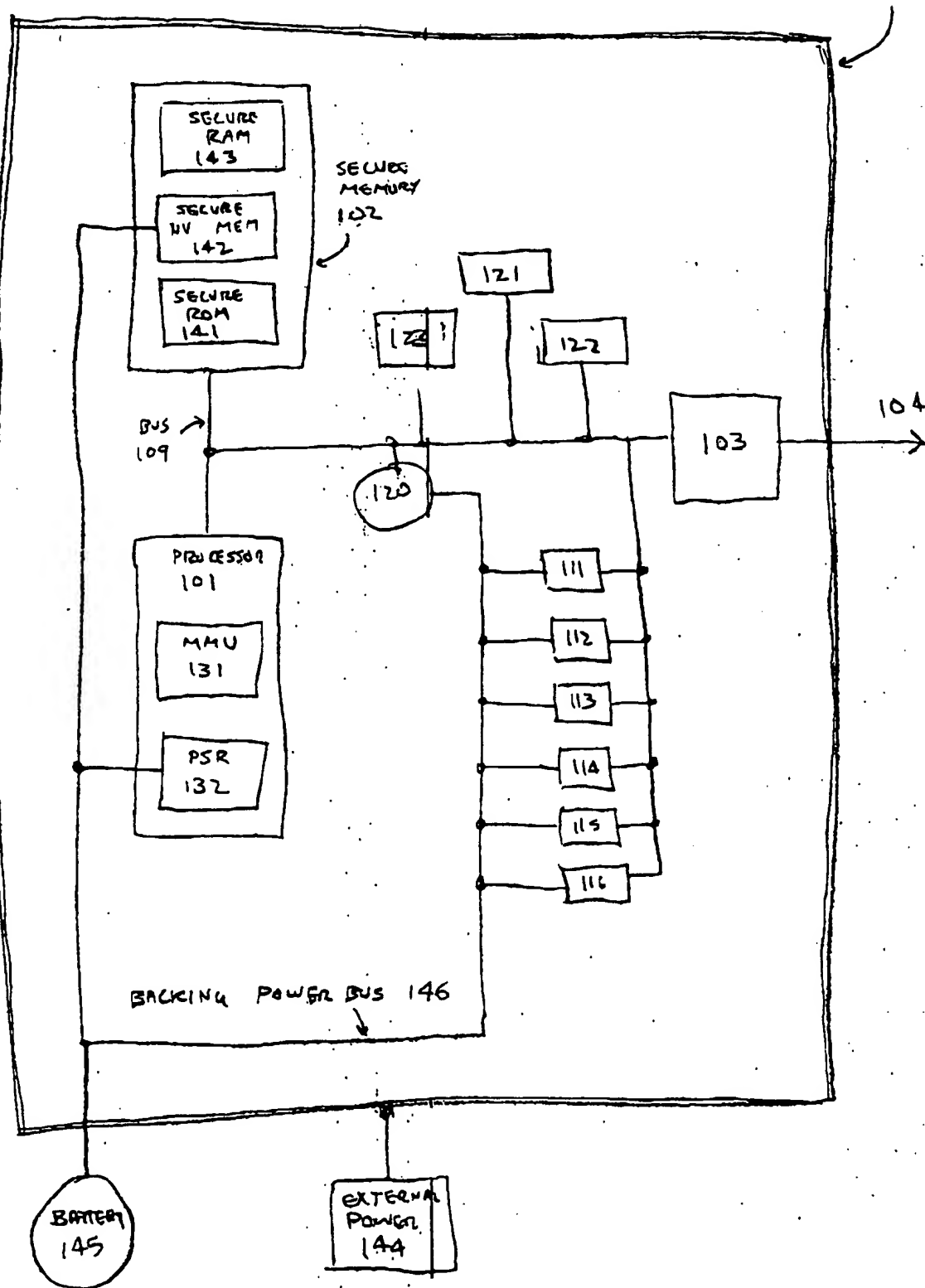
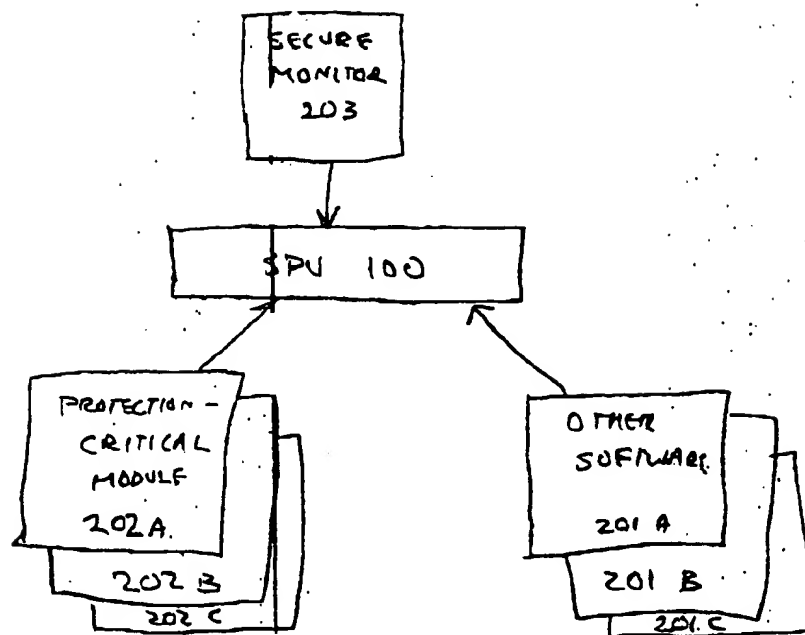


FIGURE 3



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FIGURE 4

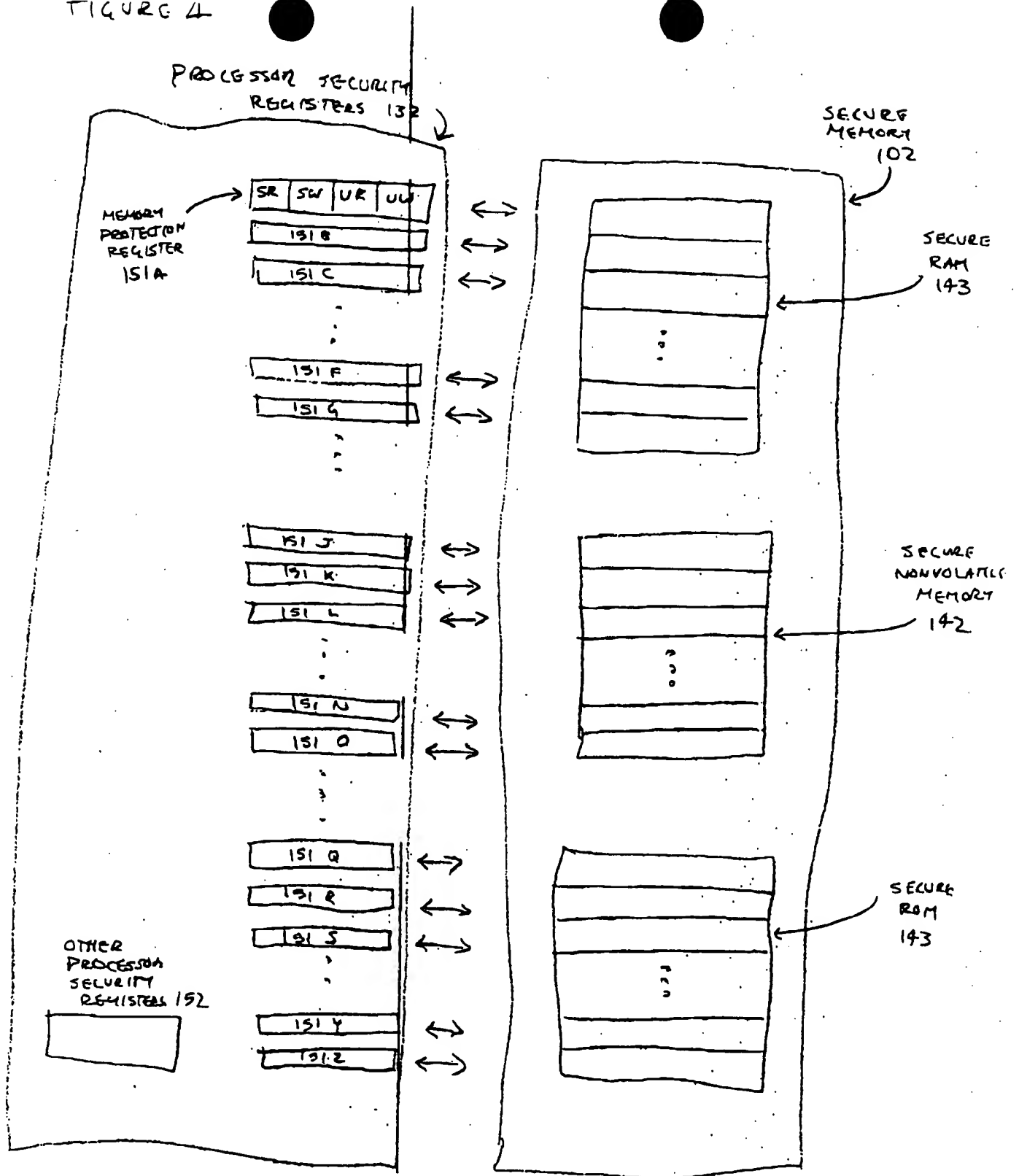


FIGURE 5

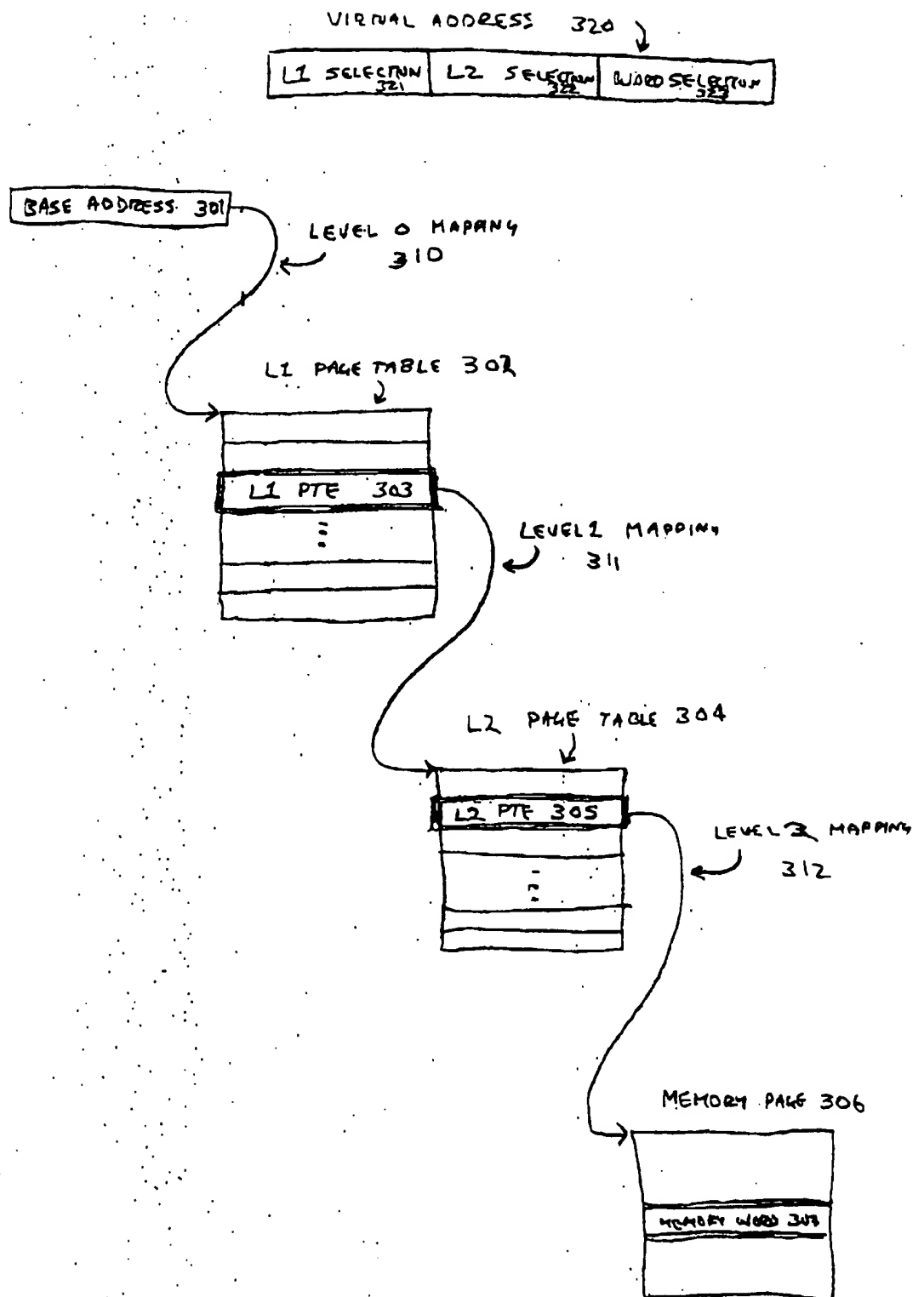


FIGURE 6

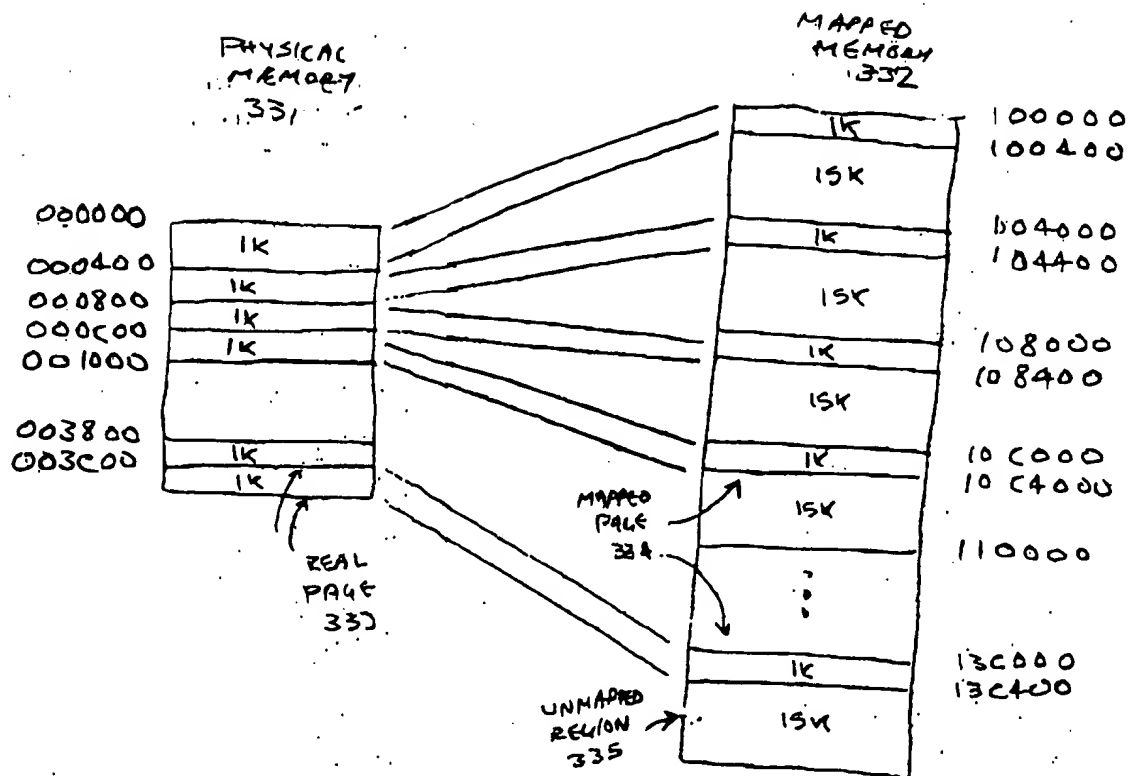


Figure 7

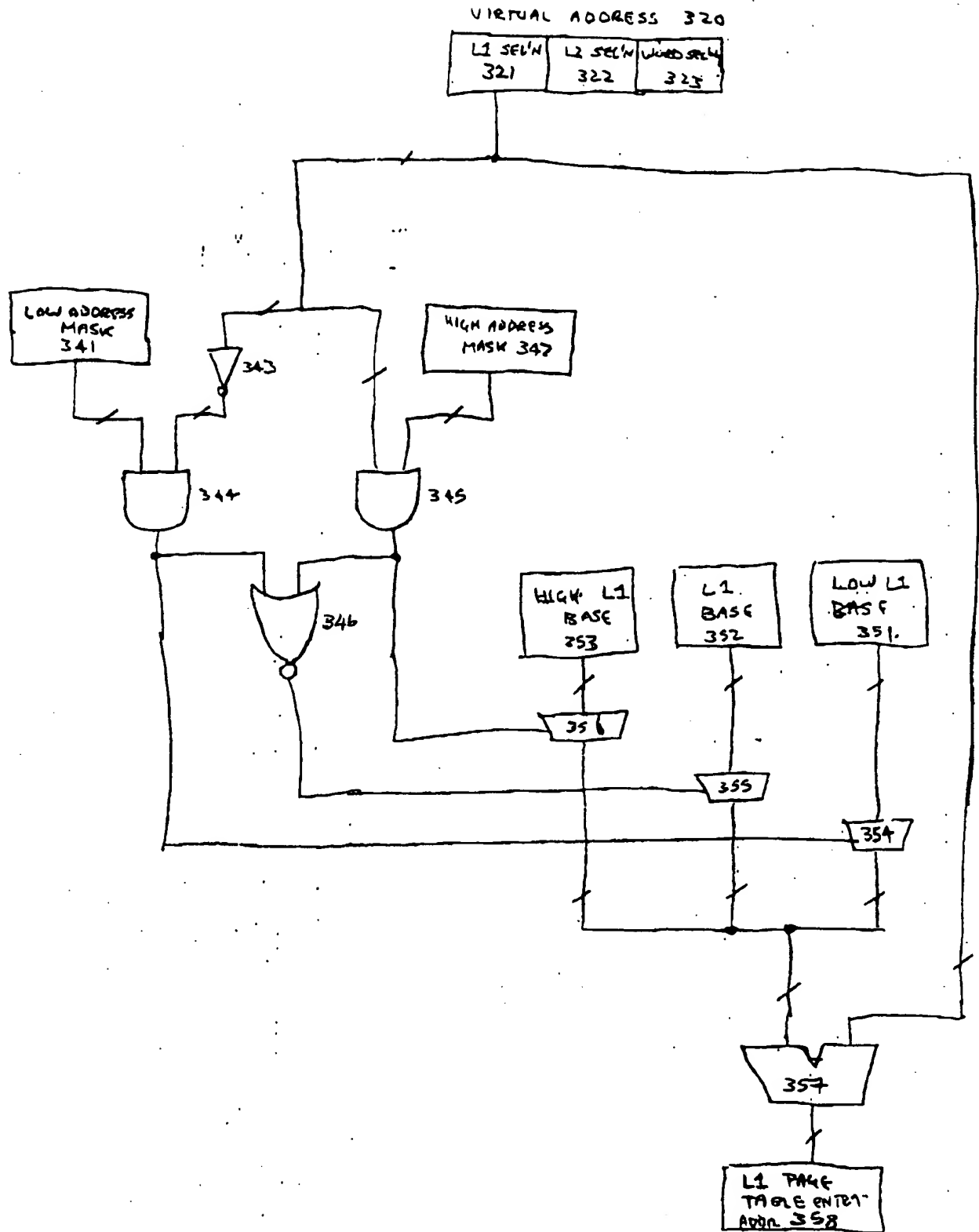


FIGURE 8

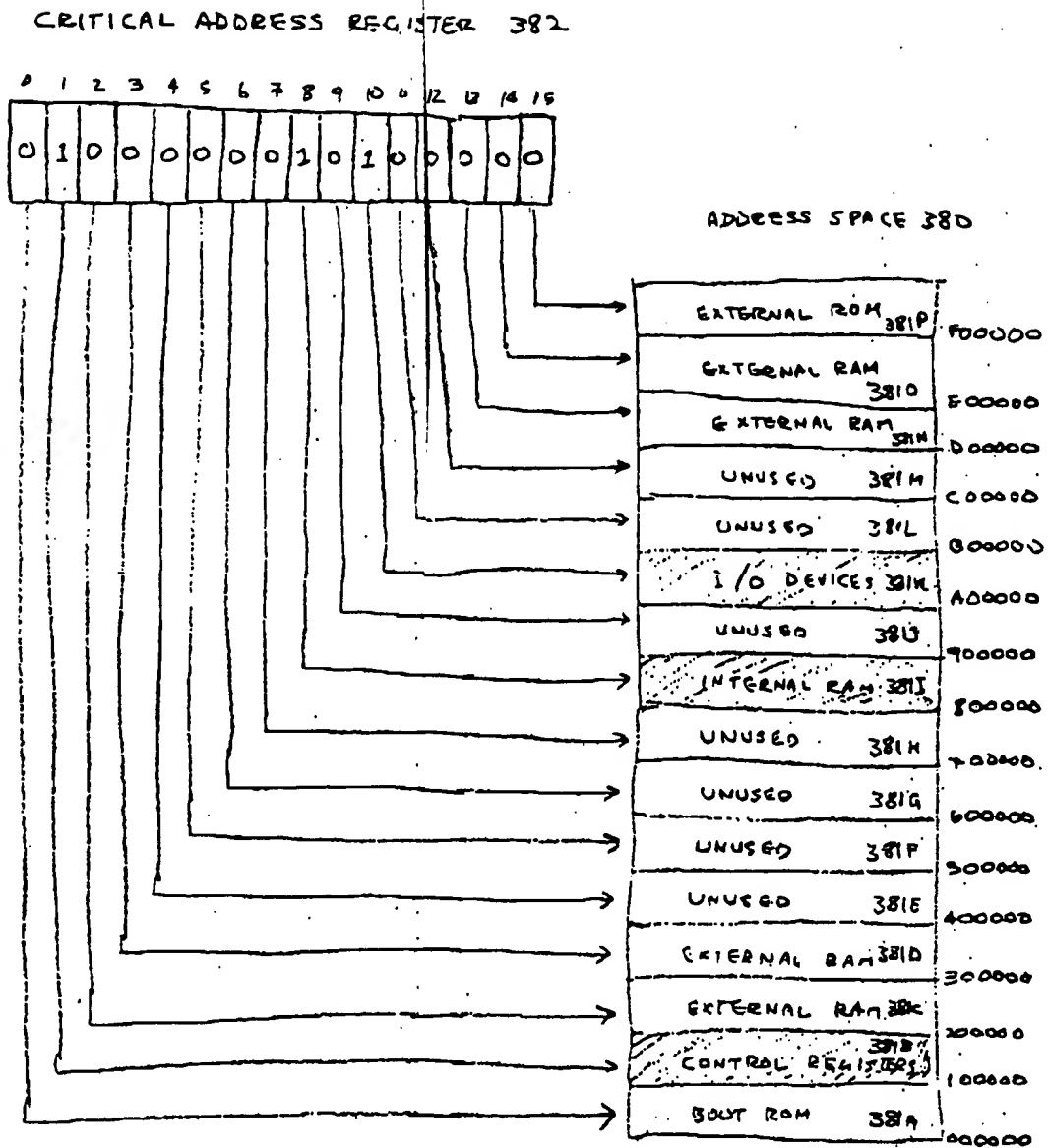


FIGURE 9

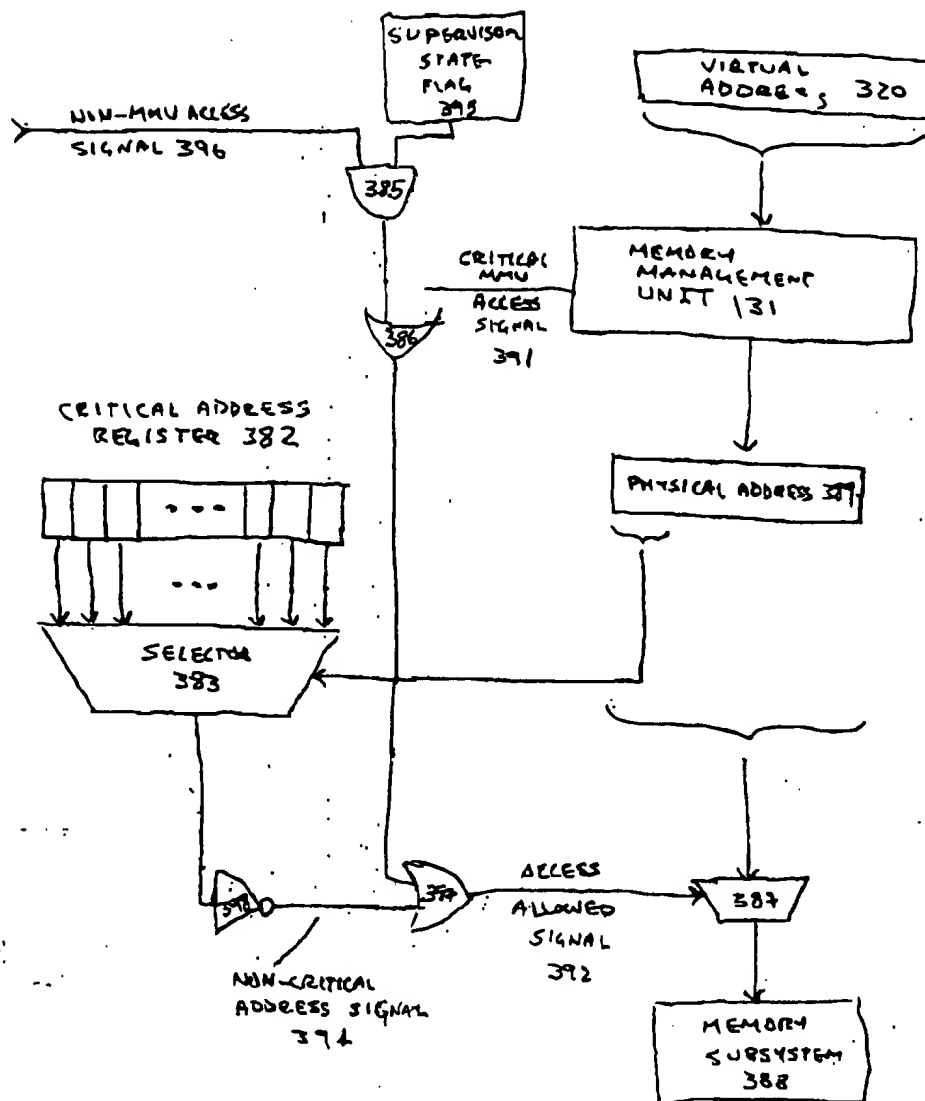


FIGURE 10

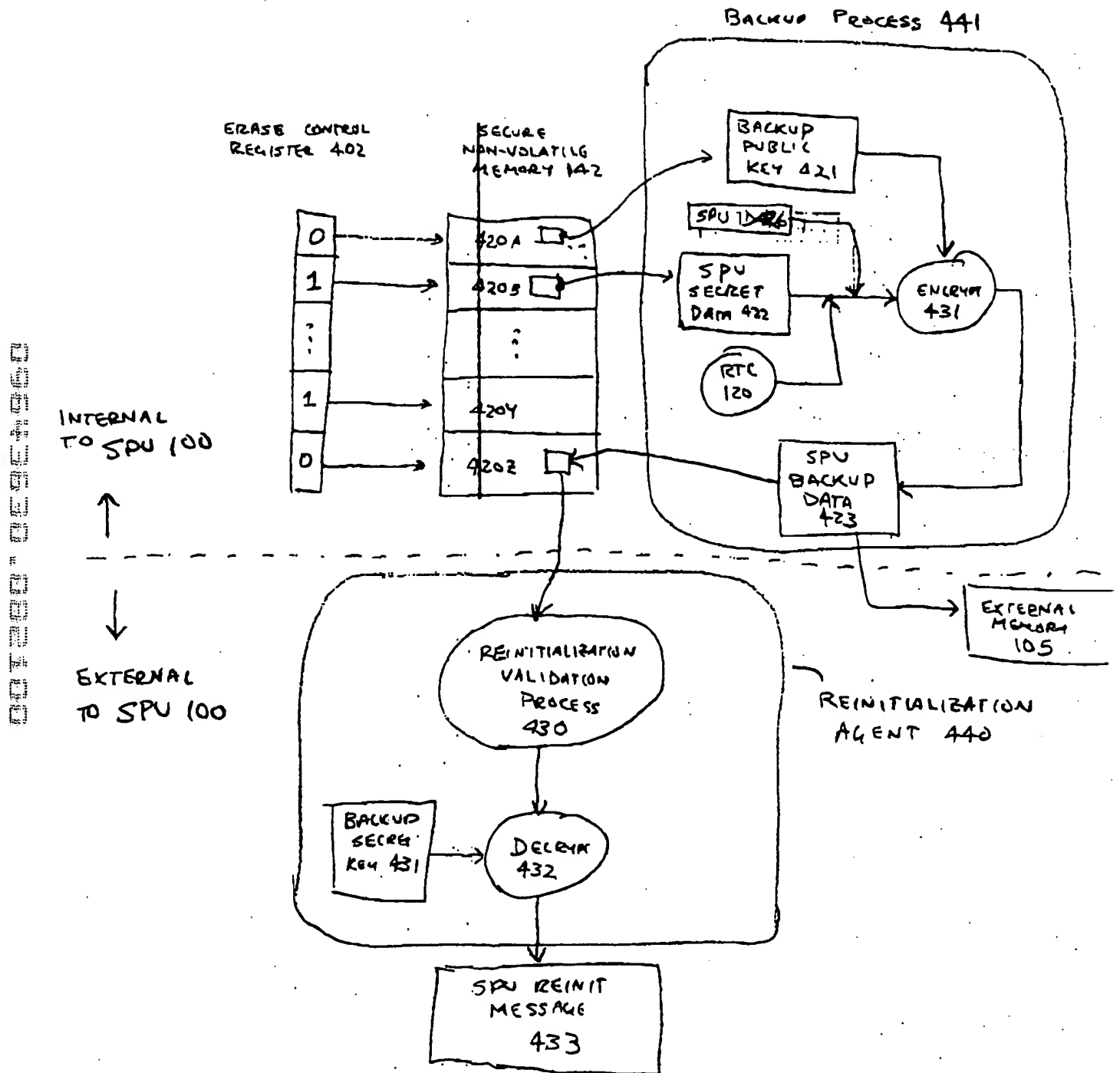


FIGURE 11

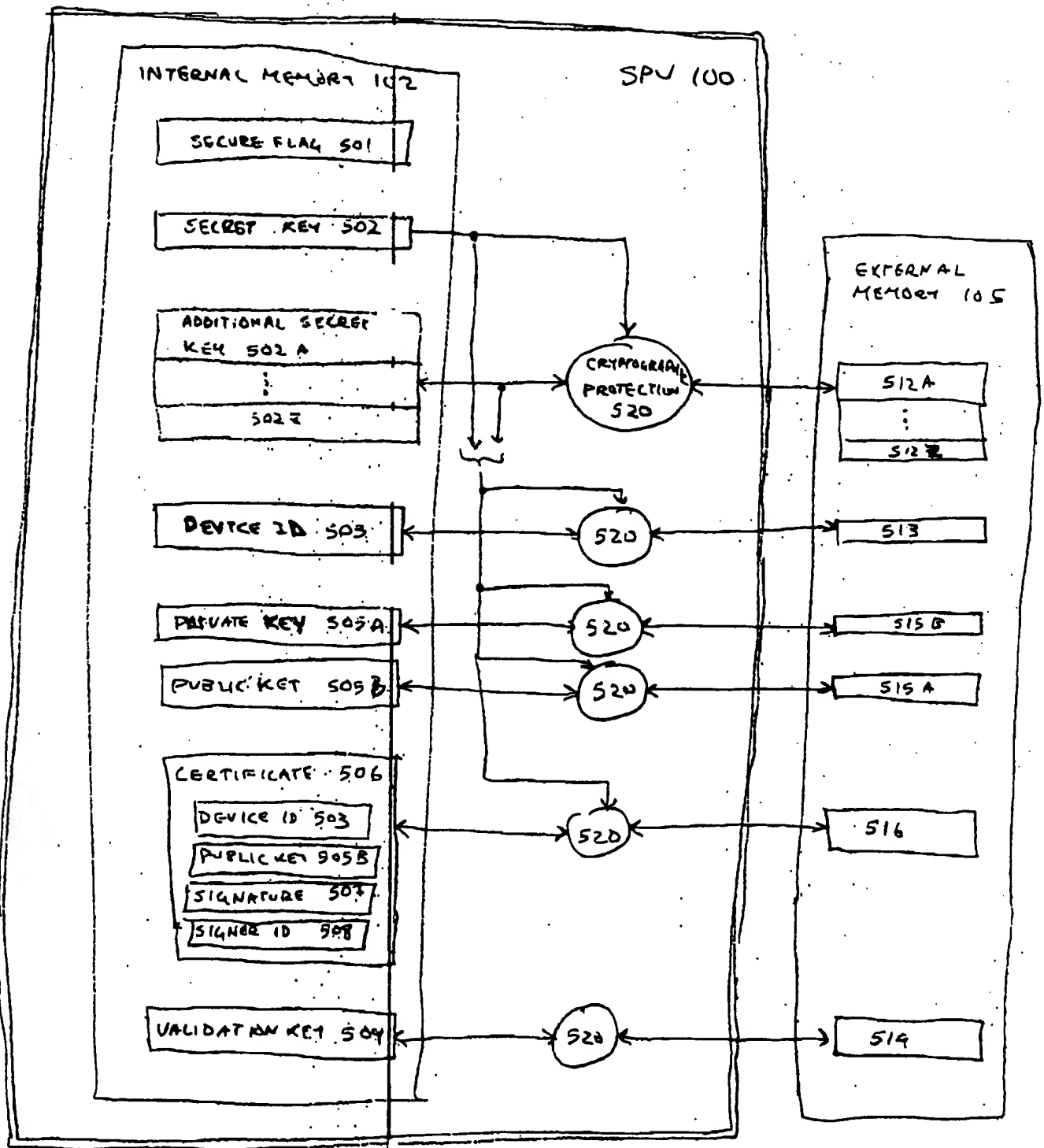


FIGURE 12

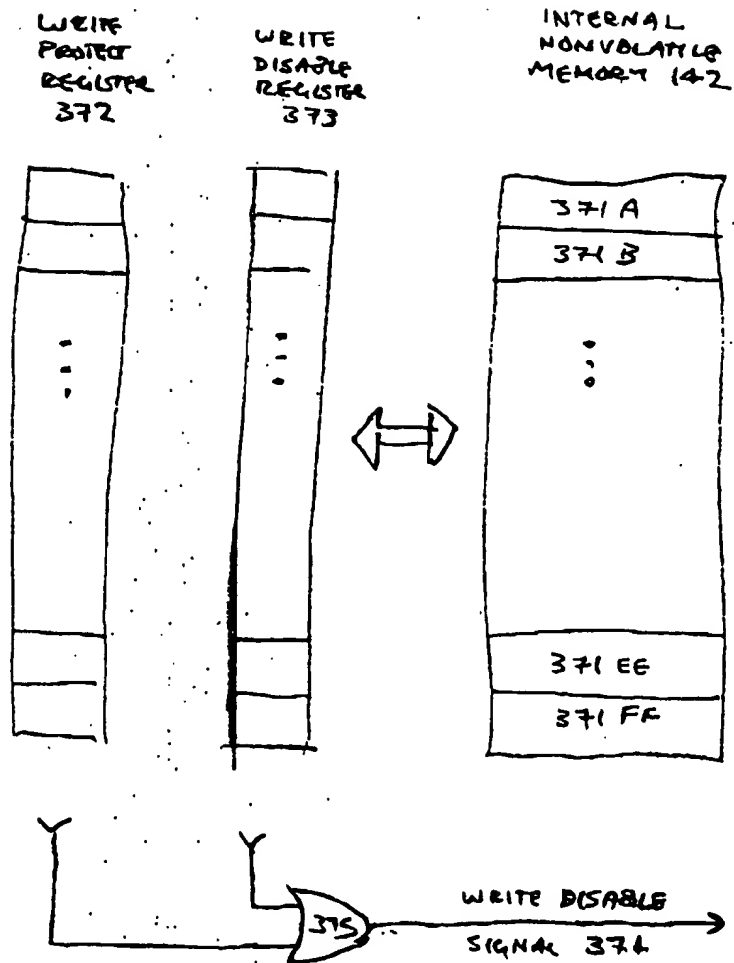


FIGURE 13 - PROTECTED ROM PARTITIONING

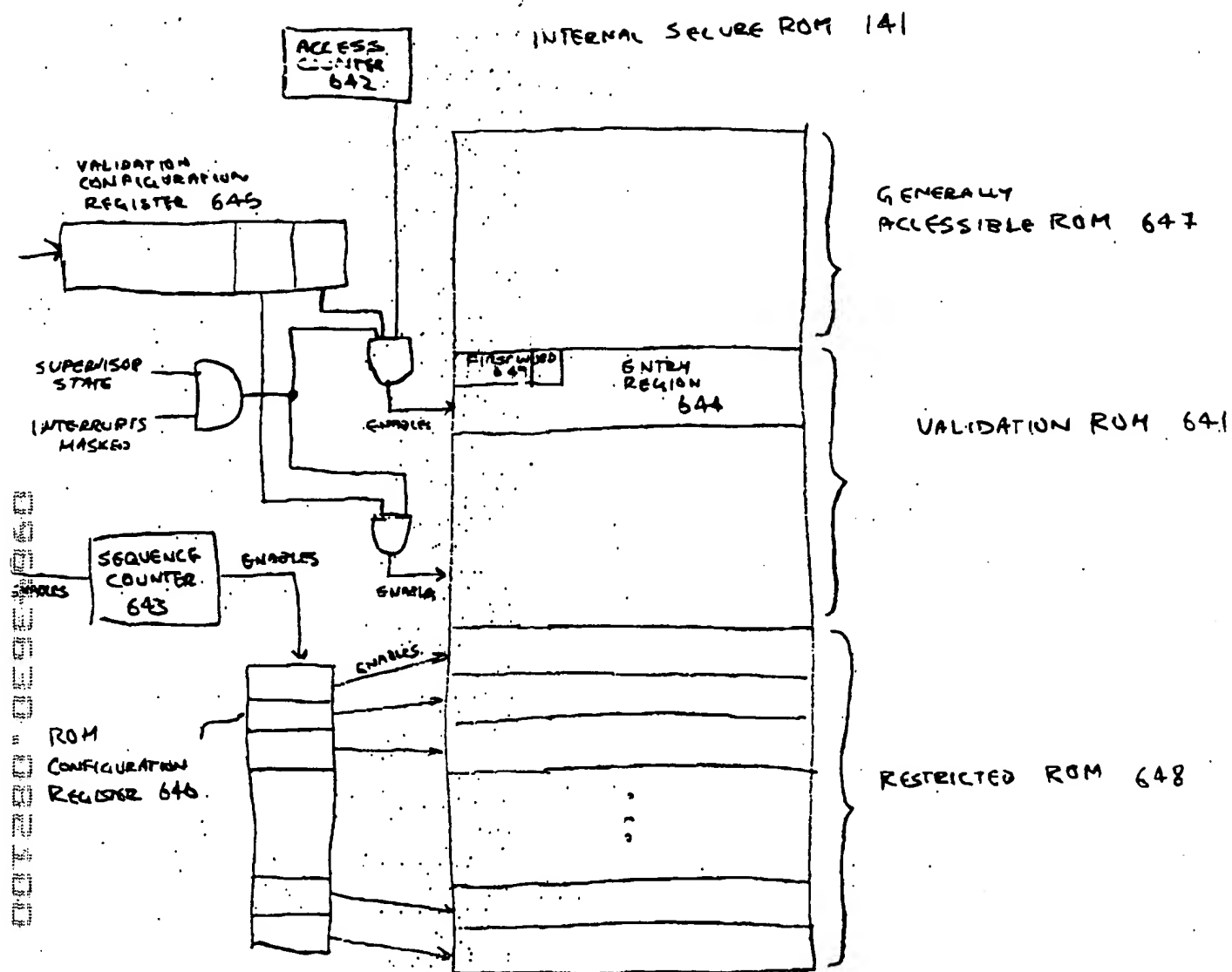


FIGURE 14 - AUTHORIZATION PROOF COMPONENTS

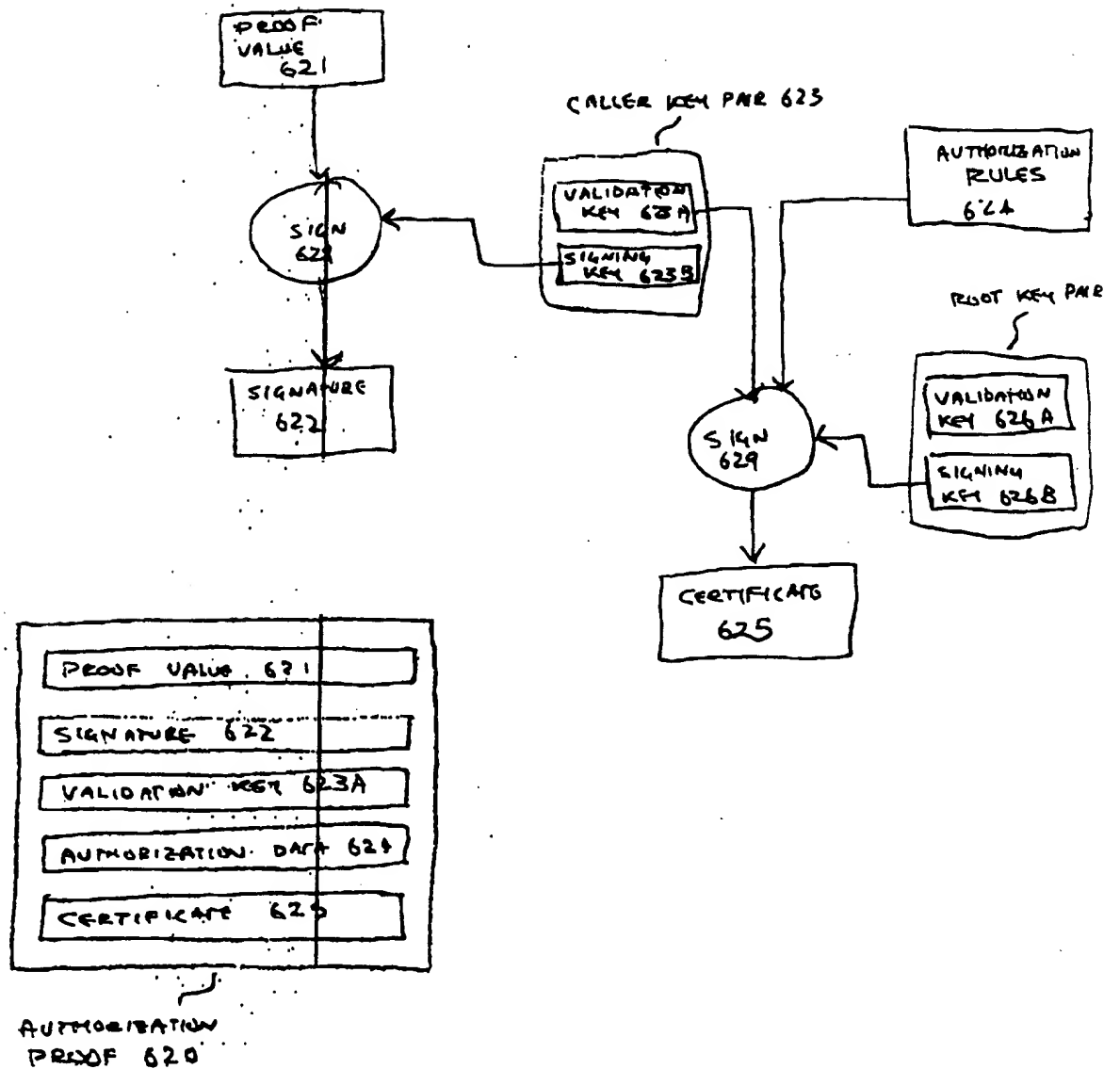


FIGURE 15 -- VALIDATION OPERATION

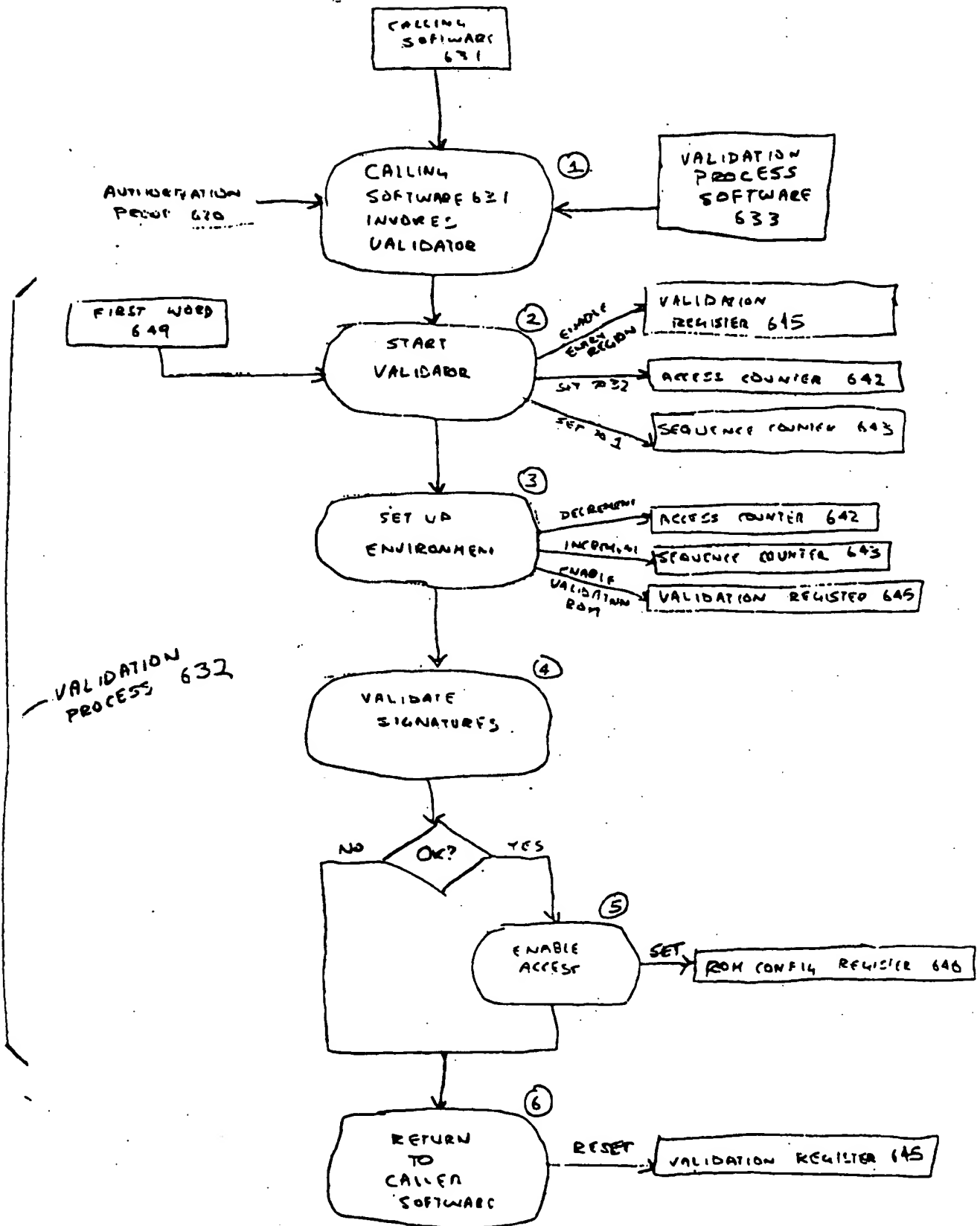
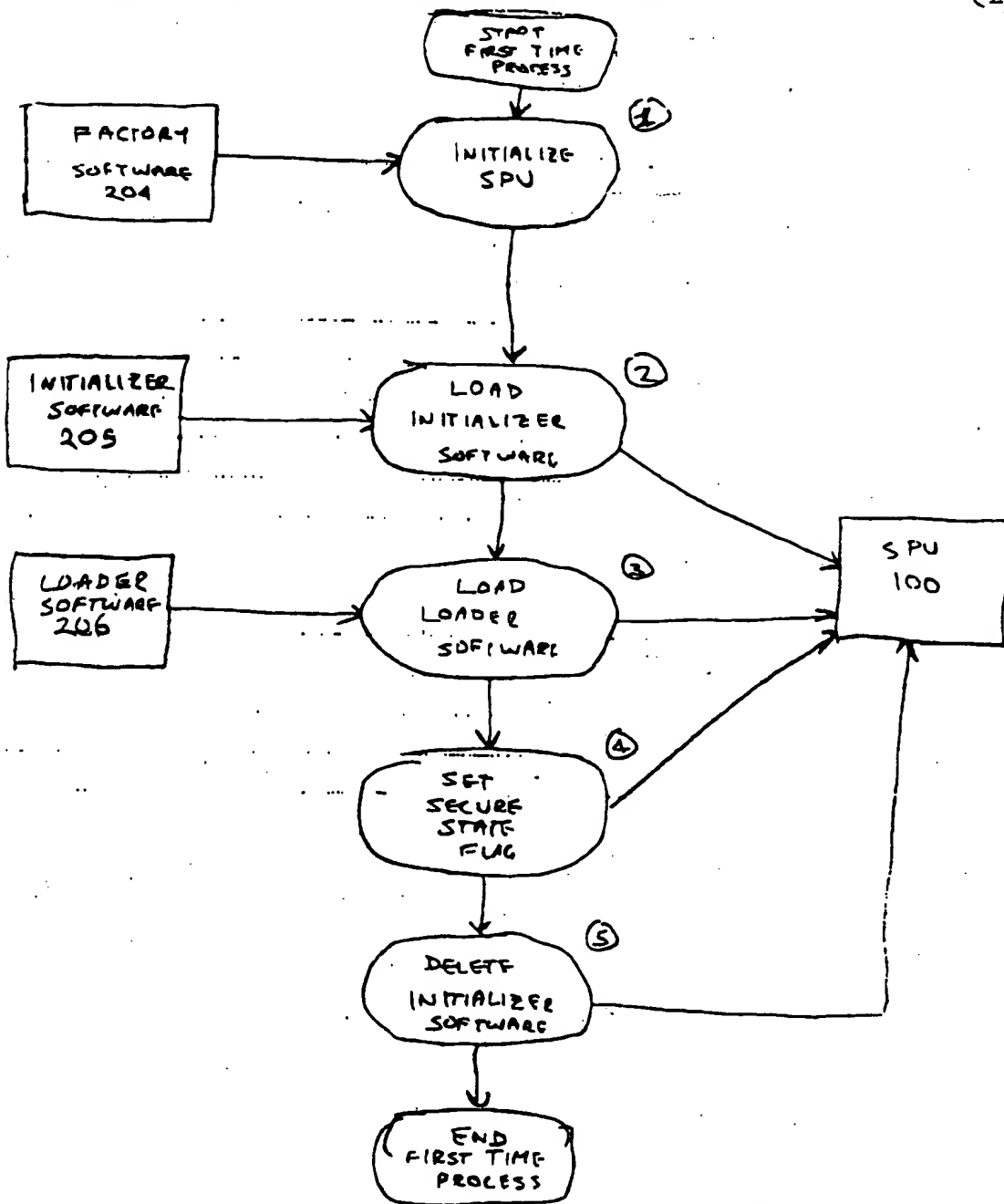


FIGURE 16 - MONITOR SOFTWARE LOAD PROCESS (1)



(The page contains faint, illegible markings or bleed-through from the reverse side.)

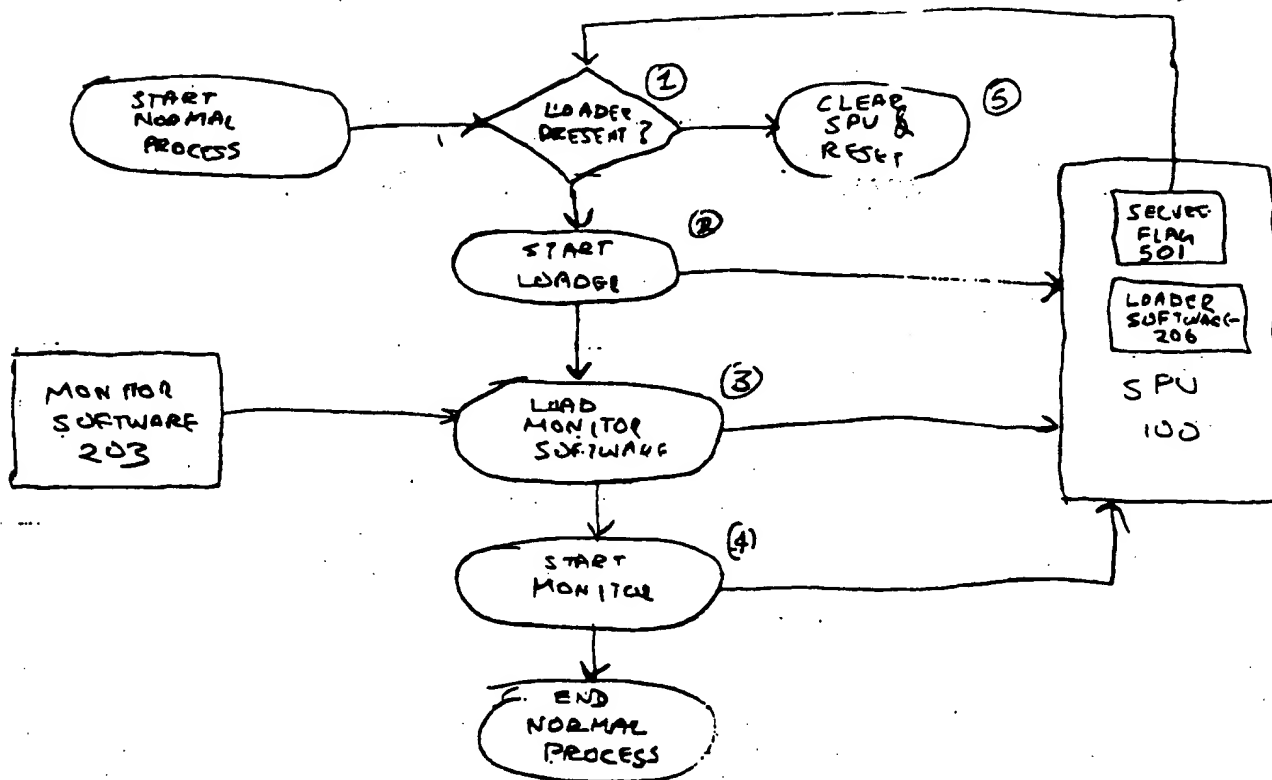


FIGURE 18-A ALTERNATING LOAD PROCESS (1)

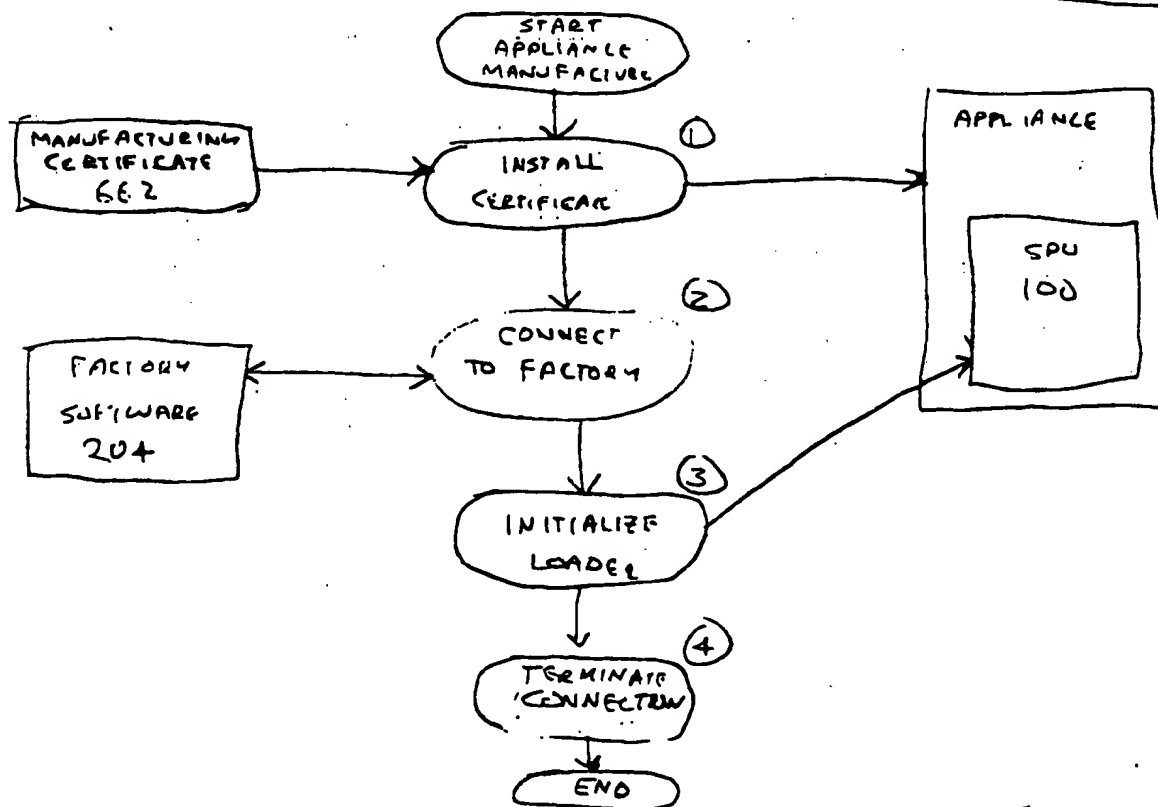
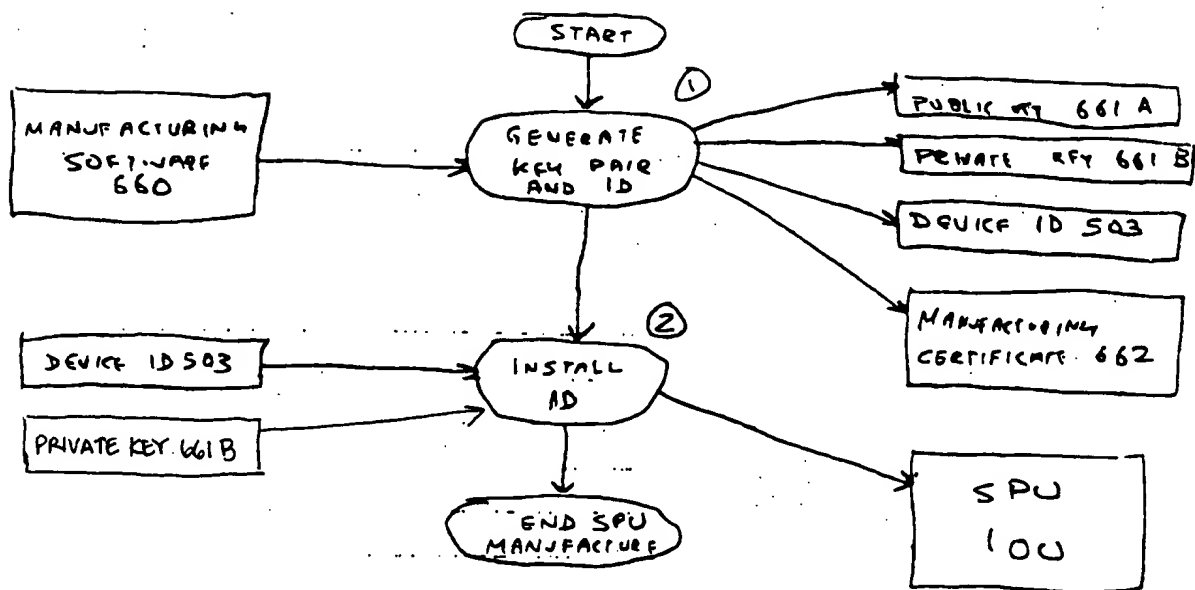


FIGURE 18-B - ALTERNATING LOAD PROCESS (2)